Image

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

s wing i	IMAMOTO et al.)
* 50 × 50	aliantian Number 10/030 214) Art unit: 2823
* UENEXP	olication Number: 10/020,314) Examiner:
File	d: December 18, 2001)
-) NGUYEN, KHIEM D
For	: FABRICATING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUITS)
)
AT	TORNEY DOCKET NO. HITA.0143)
Ho	norable Assistant Commissioner	
	Patents	
Wa	shington, D.C. 20231	•
	COVER LETTE	R
Sir:		
	The fee for submission of additional claims is calculated	

For	TOTAL WITH NEW CLAIMS ADDED	TOTAL CURRENTLY ON FILE	CLAIMS PAID	RATE	CALCULATION
Total Claims	20	20	(Over 20)	x \$18	0
Independent Claims	. 2	2	(Over 3)	x \$84	0
MULTIPLE DEPENDENT CLAIM(S)				+ \$280	0
	LING BY SMALL ENTITY IFIED STATEMENT MUS	(note 37 C.F.R. §§ 1.9, 1.27 T BE ATTACHED	, 1.28).	x ½	
			ТОТА	L	0.00

In addition, the below-identified communications are submitted in the above-captioned application or proceeding:

[x] Response to Office Action	[] Petition for Extension of Time
(with Claim Amendments)	[] Terminal Disclaimer
[] Preliminary Amendment	[] Letter to Draftsperson
[] Substitute Specification	[] Assignment
[] Other	[] Petition under

[]	Please charge my Deposit Account Num A duplicate copy	ber in the amount of to cover the fees for of this paper is enclosed.			
[]	A check in the amount of \$0.00 to cover	he fee is enclosed.			
[x]	The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to Deposit Account Number 08-1480 .				
		Respectfully submitted,			
		Stanley P. Fisher Registration Number 24,344 Juan Carlos A Marquer Registration Number 34,072			
	D SMITH LLP D Fairview Park Drive, Suite 1400				

Falls Church, Virginia 22042 (703) 641-4200

December 15, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PE	In re U.S. Patent Application of)	
) · , , 5 ¹	м д БНІМАМОТО et al.)	
DEC.	<i>Ĕ</i> /)	Art Unit 2823
P ₁	Application Number: 10/020,314)	
PATENT & TE)	Examiner
	Filed: December 18, 2001)	
)	Nguyen, Khiem D.
•	For: FABRICATING METHOD OF)	
	SEMICONDUCTOR INTEGRATED CIRCUITS)	
)	
	Attorney Docket No. HITA.0143)	
	Honorable Assistant Commissioner		
	for Patents		
	Washington D.C. 20231		

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This is in response to the office action dated October 21, 2003, in the above identified application, the period for response is set to expire on January 21, 2004. Please amend the above identified application as follows: